Intern Requirement

Preferred Discipline	• Computer / Electrical Engineering
Prerequisites/ Skills Required:	• Basic VHDL programming • Knowledge in digital designing and computer architecture
No. of students required	• 1 student

Project Details

Title	Digital Designs on FPGAs
Overview/Background	• As the capabilities of FPGAs have increased, they are becoming increasingly popular in the field of high performance computing. FPGAs have the capability to match a specific application's parallel processing needs, as applications today require machines with heterogeneous capabilities. However, these attractive advantages come with a cost, as fine grained digital designing skills are required.
Aims/Objectives/Deliverables	• To research into various digital circuits by conducting literature survey, demonstrating the circuits in FPGAs and evaluating their performance.
Scope	• Digital circuits of interest include ALUs, string matching, SDRAM communication. The intern will have the opportunity to use simulation tools and conduct hands-on experiments using FPGAs.
Project Duration	• 2-3 months